# DIN W48×H48mm Compact Counter/Timer

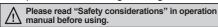
### Features

- Counting speed: 1cps/30cps/2kcps/5kcps
- Selectable voltage input (PNP) or no-voltage input (NPN)
- Input mode: Up, Down, Up/Down
- Dot for Decimal Point / Hour. Min. Second by RESET key
- Wide range of input power supply
- : 100-240VAC 50/60Hz, 24VAC 50/60Hz, 24-48VDC universal • Selectable Counter/Timer by internal DIP switch
- [Counter]
- 20 input modes/18 output modes
- [Timer]

16 output modes

Various time setting range - 5-digit model: 0.01 sec to 9999.9 hour / 4-digit model: 0.01 sec to 9999 hour

• Output: Indicator, 1-stage setting





Model	Display digit	Size	Output	Power supply	
FX4S-1P2	9999 (4-digit)	DIN W48×H48mm	1-stage setting	24VAC 50/60Hz, 24-48VDC	
FX4S-1P4				100-240VAC 50/60Hz	
FX5S-I2	00000 (E digit)		Indicator	24VAC 50/60Hz, 24-48VDC	
FX5S-I4	99999 (5-digit)			100-240VAC 50/60Hz	

# Specifications

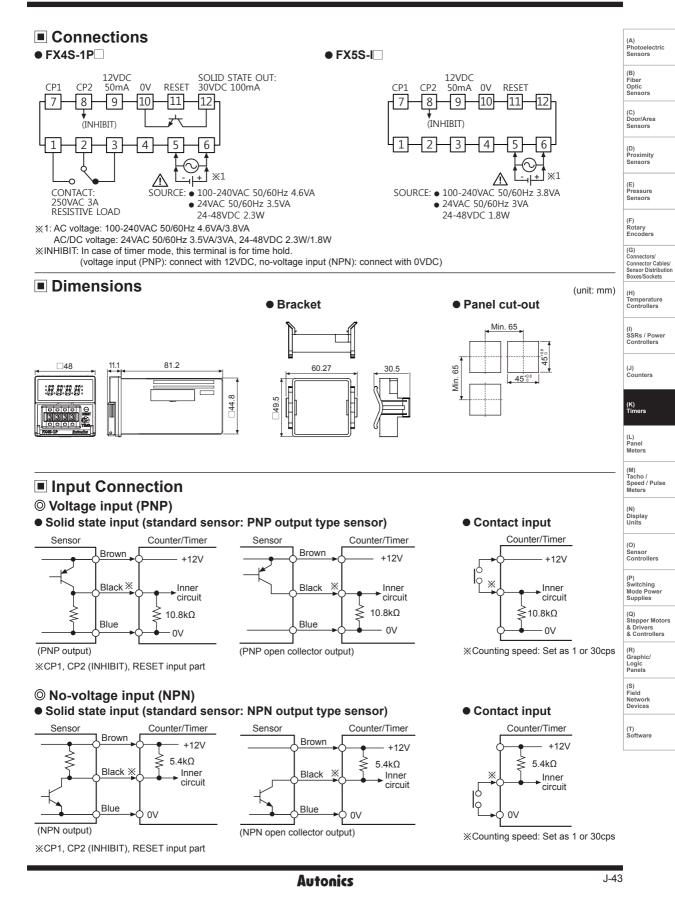
Madal	1-stage se	tting	FX4S-1P2	FX4S-1P4	—	<u> </u>	
Model	Indicator			— —	FX5S-I2	FX5S-I4	
Display digit			4-digit		5-digit	1	
Character siz	e (W×H)		3.8×7.6mm		4×8mm		
Power supply	/		24VAC~ 50/60Hz, 24-48VDC	100-240VAC~ 50/60Hz	24VAC~ 50/60Hz, 24-48VDC	100-240VAC~ 50/60Hz	
Permissible v	oltage rang	е	90 to 110% of rated v	oltage	- <b>i</b> .		
Power consu	mption		AC: Max. 3.5VA DC: Max. 2.3W	Max. 4.6VA	AC: Max. 3VA DC: Max. 1.8W	Max. 3.8VA	
Max. counting	g speed of (	CP1/CP2	Selectable 1cps/30cps/2kcps/5kcps (DIP switch)				
Return time	-		Max. 500ms				
Min. signal w	idth		INHIBIT, RESET inpu	t: approx. 20ms			
Input method		Selectable voltage input (PNP) method or no-voltage input (NPN) method [Voltage input (PNP) method]-input impedance: max. 10.8kΩ, [H]: 5-30VDC=, [L]: 0-2VDC [No-voltage input (NPN) method]-short-circuit impedance: max. 470Ω, short-circuit residual voltage: max. 1VDC, open-circuit impedance: min. 100kΩ					
One-shot out	put time		0.05 to 5 sec				
		Туре	Instantaneous SPDT	nstantaneous SPDT (1c)			
Control	Contact	Capacity	250VAC~ 3A resistive	250VAC~ 3A resistive load —			
output	Solid	Туре	VPN open collector: 1				
1	state	Capacity	Max. 30VDC, 100m	A	—		
Relay	Mechanica	al	Min. 5,000,000 operations				
life cycle	Electrical		Min. 100,000 operations (250VAC 3A resistive load)				
Repeat/Set/Vo	oltage/Tempe	erature error	Max. ±0.01% ±0.05 sec				
Insulation res	sistance		Over 100MΩ (at 500VDC megger)				
External pow	er supply		Max. 12VDC== ±10% 50mA				
Memory reter	ntion		Approx. 10 years (non-volatile memory)				
Dielectric stre	ength		2,000VAC 50/60Hz for 1 minute (between all terminals and case)				
Noise	AC voltage		$\pm 2kV$ the square wave noise (pulse width 1µs) by the noise simulator				
immunity	AC/DC vo		$\pm 500V$ the square wave noise (pulse width 1µs) by the noise simulator				
Vibration	Mechanica		0.75mm amplitude at frequency 10 to 55Hz (for 1 min) in each X, Y, Z direction for 1hour				
violation	Malfunctio	n	0.5mm amplitude at frequency 10 to 55Hz (for 1 min) in each X, Y, Z direction for 10min				
Shock	Mechanica			G) in each X, Y, Z direction for			
Malfunction		100m/s <sup>2</sup> (approx. 10G) in each X, Y, Z direction for 3 times					
Environment			-10 to 55°C, storage: -25 to 65°C				
	Ambient h	umidity	35 to 85%RH, storage: 35 to 85%RH				
Protection str	ucture		IP20 (front part, IEC standard)				
Approval							
Weight <sup>**1</sup>		Approx. 171g (approx	( 110a)	Approx. 156g (approx	(05a)		

Shaded parts() are changed and added functions from previous FXS Series.

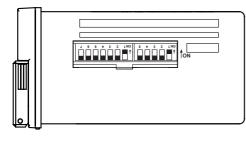
Upgrade

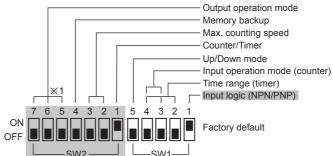


# **Up/Down Counter/Timer**



## DIP Switch Setting





※1: Indicator model (FXS5-I□) does not have no. 5, 6, 7 of SW2. for output operation mode setting.

#### • Max. counting speed

SW2	3 2	3 2	3 2	3 2
	ON	ON	ON	ON
	OFF	OFF	OFF	OFF
Function	1cps	30cps	2kcps	5kcps

#### • Time range (timer)

• • •				
SW1	FX4S-1P	FX5S-I		
4 3 2 ON OFF	99.99sec	9999.9sec		
4 3 2 ON OFF	999.9sec	99999sec		
4 3 2 ON OFF	9999sec	9min 59.99sec		
4 3 2 ON OFF	99min 59sec	99min 59.9sec		
4 3 2 ON OFF	999.9min	9999.9min		
4 3 2 ON OFF	99hour 59min	9hour 59min 59sec		
4 3 2 ON OFF	999.9hour	999hour 59min		
4 3 2 ON OFF	9999hour	9999.9hour		

# Input logic (CP1\_CP2\_INHIBIT\_RESET input)

	(CP1, CP2, INFIDIT, RESET INPUL)				
SW1		Function			
1	ON OFF	NPN (No-voltage input)			
1	ON OFF	PNP (voltage input)			

### • Up/Down mode

SW	/1	Function
5	ON OFF	Down mode
Ð	ON OFF	Up mode

### • Counter/Timer

SW2		Function
4	ON OFF	Counter mode
1	ON OFF	Timer mode

### Memory backup

SW	/2	Function
4	ON OFF	No memory backup
4	ON OFF	Memory backup

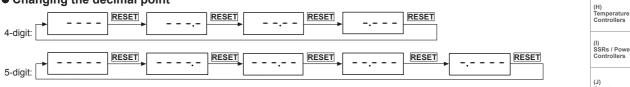
#### Dot For Decimal Point / Hour. Min. Second **RUN** mode RESET 3sec RESET 3sec dP Setting mode RESET Counter mode Timer mode Set decimal point RESET ELr SEE by front RESET Hour/Min/Sec are Hour/Min/Sec are not divided with dot. E.g.) 0.59.59: 59min 59sec divided with dot. E.g.) 5959: 59min 59sec

\*Run mode: hold the RESET key for over 3 sec, and it enters setting mode [dP].

\*Setting mode: hold the **RESET** key for over 3 sec, and it saves the setting and returns to RUN mode.

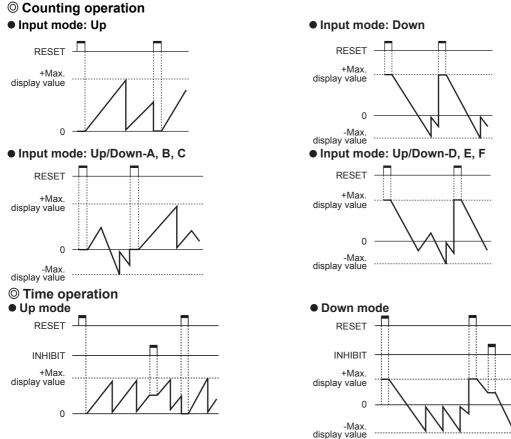
If there is no **RESET** key input for 60 sec when entering setting mode, it returns to RUN mode.

### Changing the decimal point



XIt returns to RUN mode if no RESET key or digital switch is applied for 60 sec in decimal point setting status.

# Counting & Time Operation For Indicator (FX5S-I)



(M) Tacho / Speed / Pulse Meters

(A) Photoelectric Sensors

(B) Fiber Optic Sensors

(C) Door/Area Sensors

(D) Proximity Sensors

(E) Pressure Sensors

(F) Rotary Encoder

(J) Counters

(K) Timers

(L) Panel Meters

(G) Connectors/ Connector Cables/ Sensor Distribution Boxes/Sockets

(N) Display Units

(O) Sensor Controllers

(P) Switching Mode Powe Supplies

(Q) Stepper Motors & Drivers & Controllers

(R) Graphic/ Logic Panels

(S) Field Network Devices

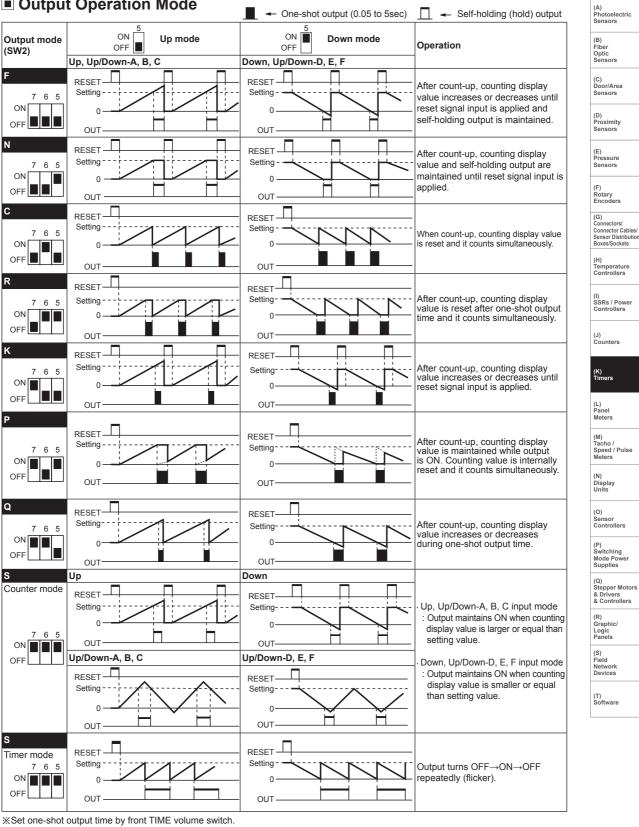
(T) Software

# Input Operation Mode (counter)

			ode (counter)	CP: Clock Pulse
Input mode		SW1	Voltage input (PNP) method	No-voltage input (NPN) method
Up mode ON OFF	Up/Down-A (command input)	ON OFF	$CP1 \downarrow \qquad $	$CP1 \stackrel{H}{ \rule{0.5mm}{.5mm}} \xrightarrow{P1} \stackrel{P1}{ 0.$
	Up/Down-B (individual input)	4 3 ON OFF	$CP1 \downarrow CP2 \downarrow Count 0 1 2 1 1 2 3 2 1 1 1 2 3 2 1 1 1 1$	$\begin{array}{c} CP1 \stackrel{H}{\vdash} \\ CP2 \stackrel{H}{\vdash} \\ \hline \\ CP2 \stackrel{H}{\vdash} \\ \hline \\ Count \\ 0 \\ \hline \\ \end{array}$
	Up/Down-C (phase difference input)	ON OFF	$CP1 \downarrow \\ BBBB \\ CP2 \downarrow \\ CP1 \downarrow \\ BBBB \\ CP2 \downarrow \\ CP1 \downarrow $	$CP1 \stackrel{H}{\vdash} \stackrel{I}{\vdash} $
	Up	A 3 ON OFF	CP1 $\overset{H}{}$ $\overset{A}{}$ {	$CP1 \downarrow \downarrow$
	(adding input)		CP1 $H$ No counting CP2 $H$ $A_{+}$ $A_{+}$ CP2 $H$ $A_{+}$	$CP1 H = No counting $ $CP2 H = A_{4}$ $CP2 H$
Down mode ON OFF	Up/Down-D (command input)	ON OFF	CP1 $\overset{H}{\overset{h}{\overset{h}{\overset{h}{\overset{h}{\overset{h}{\overset{h}{\overset{h}{$	$\begin{array}{c} \text{CP1} H \\ \text{CP2} H \\ \text{CP2} H \\ \text{Count} \\ 0 \end{array}$
	Up/Down-E (individual input)	ON A 3 OFF	CP1 $\overset{\text{H}}{\overset{\text{L}}_{\text{L}}}$ $\overbrace{\text{CP2}}^{\text{P1}}$ $\overbrace{\overset{\text{H}}{\overset{\text{L}}_{\text{L}}}}$ $\overbrace{\overset{\text{H}}{\overset{\text{L}}_{\text{L}}}}$ $\overbrace{\overset{\text{H}}{\overset{\text{L}}_{\text{L}}}}$ $\overbrace{\overset{\text{H}}{\overset{\text{H}}_{\text{L}}}}$ $\overbrace{\overset{\text{H}}{\overset{\text{H}}_{\text{H}}}}$ $\overbrace{\overset{\text{H}}}}$ $\overbrace{\overset{\text{H}}{\overset{\text{H}}}}$ $\overbrace{\overset{\text{H}}{\overset{\text{H}}}}$ $\overbrace{\overset{\text{H}}}}$ $\overbrace{\overset{\text{H}}{\overset{\text{H}}}}$ $\overbrace{\overset{\text{H}}}$ $\overbrace{\overset{\text{H}}}}$ $\overbrace{\overset{\text{H}}}$ $\overbrace{\overset{\text{H}}}}$ $\overbrace{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}{}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}{}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}{}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}{}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}{}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}{}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}{}$ $\underset{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}{}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}{}$ } $\underset{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}$ } $\underset{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}}$ $\underset{\overset{\text{H}}}$ $\underset{\overset{\text{H}}}$ }  }  } $\underset{\overset{\text{H}}}$ }  }  }  }  }  }  }  }  }	$\begin{array}{c} \text{CP1} \stackrel{\text{H}}{\underset{\text{L}}{\overset{\text{H}}}} & \stackrel{\text{H}}{\underset{\text{CP2}}{\overset{\text{H}}{\underset{\text{L}}{\overset{\text{H}}}}} & \stackrel{\text{H}}{\underset{\text{CP2}}{\overset{\text{H}}{\underset{\text{L}}{\overset{\text{H}}}}} & \stackrel{\text{H}}{\underset{\text{CP2}}{\overset{\text{H}}{\underset{\text{L}}{\overset{\text{H}}}}} & \stackrel{\text{H}}{\underset{\text{CP2}}{\overset{\text{H}}{\underset{\text{L}}{\overset{\text{H}}{\underset{\text{L}}{\overset{\text{H}}{\underset{\text{L}}{\overset{\text{H}}{\underset{\text{L}}{\overset{\text{H}}{\underset{\text{L}}{\underset{\text{L}}{\overset{\text{H}}{\underset{\text{L}}}{\underset{\text{L}}{\underset{\text{L}}{\underset{\text{L}}}{\underset{\underset{L}}{\underset{\text{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{L}}{\underset{\underset{\underset{L}}{\atop\atop{L}}{\underset{\underset{\underset{\underset{L}}}{\underset{\underset{\underset{\underset{L}}}{\underset{\underset{\underset{\underset$
	Up/Down-F (phase difference input)	4 3 ON	$CP1 \downarrow \square $	$CP1 \stackrel{H}{{}{}{}{}{}{}{$
	Down (subtracting input)		CP1 $H$ $A_{\mu}$ $A_{\mu}$ CP2 $H$ $No counting$ Count $No counting$ Count $No counting$	$\begin{array}{c c} CP1 \stackrel{H}{\vdash} \\ \hline \\ CP2 \stackrel{H}{\vdash} \\ \hline \\ Count \\ \hline \\ 0 \end{array} n \\ n-1 \\ n-2 \\ n-3 \\ n-4 \\ n-5 \\$
		OFF	CP1 $\downarrow$ $\downarrow$ $\land$	$\begin{array}{c c} CP1 H \\  \\ P1 \\ P$

XA: over min. signal width, B: over than 1/2 of min. input signal width. If the signal is smaller than these width, it may cause counting error (±1).

# **Up/Down Counter/Timer**



## Output Operation Mode

# Proper Usage

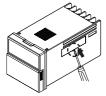
### OIP switch setting

Turn OFF the power before setting the DIP switch to the Counter/Timer.

After DIP switch setting when cutting off the power, press the front RESET key or supplying the external reset.

#### **O Detaching DIP switch cover**

%Turn OFF the power before detaching the DIP switch cover.



Push and pull the groove of DIP switch cover with a flat head driver to the front. The cover is detached from the case.  $\triangle$  Be sure not to be wounded when using a tool.

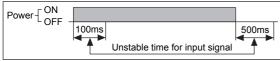
### © Error

Display	Error	Troubleshooting
ErrO		Change the setting value anything but 0.

%If error occurs, the output turns OFF.

XIndicator model does not have error display function.

#### O Power



- In case of 24VAC, 24-48VDC model, power supply should be insulated and limited voltage/current or Class 2, SELV power supply device.
- ② The inner circuit voltage rises within 100ms after supplying the power to the unit. The input may be unavailable at this period. Be sure that the inner circuit voltage drops within 500ms after turning OFF the power.



- ③ Use the unit within the rated power supply.
- When supplying or cutting the power, use a switch not to occur chattering.

### © Input signal line

- ① Shorten the cable from the sensor to the unit.
- ② Use shield cable when input cable is longer.
- ③ Wire the input signal line separately from power line.
- Testing dielectric voltage or insulation resistance when the unit is installed at control panel
- ① Isolate the unit from the circuit of control panel.
- ② Short all terminals of the unit.

# O Do not use the unit in the following environments.

- ① Environments with high vibration or shock.
- ② Environments with strong alkali or strong acid materials
- ③ Environments with exposure to direct sunlight
- ④ Near machinery which produces strong magnetic force or electric noise
- © This product may be used in the following environments.
- ① Indoor
- 2 Altitude max. 2,000m
- ③ Pollution degree 2
- ④ Installation category II