LAA100L
Dual Single-Pole OptoMOS ${ }^{\circledR}$ Relays

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 350 | $V_{P}$ |
| Load Current | 120 | $\mathrm{~mA}_{\mathrm{rms}} / \mathrm{mA}_{\mathrm{DC}}$ |
| On-Resistance (max) | 25 | $\Omega$ |

## Features

- 3750 rms $_{\text {Input/Output Isolation }}$
- Low Drive Power Requirements
- High Reliability
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Small 8-Pin DIP Package
- Flammability Rating UL 94 V-0
- Surface Mount Tape \& Reel Version Available


## Applications

- Telecommunications
- Telecom Switching
- Tip/Ring Circuits
- Modem Switching (Laptop, Notebook, Pocket Size)
- Hook Switch
- Dial Pulsing
- Ground Start
- Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls


## Description

The LAA100L is a 350 V , 120 mA , dual single-pole, normally open (1-Form-A) relay. It features low, $25 \Omega$, maximum on-resistance, and includes integrated current limiting.

## Approvals

- UL recognized: File Number E76270
- CSA Certified: File Number LR43639-10
- EN/IEC 60950-1 Certified Component: Certificate available on our website

Ordering Information

| Part \# | Description |
| :--- | :--- |
| LAA100L | 8 Pin DIP (50/Tube) |
| LAA100PL | 8-Pin Flatpack (50/Tube) |
| LAA100PLTR | 8-Pin Flatpack (1000/Reel) |
| LAA100LS | 8 Pin Surface Mount (50/Tube) |
| LAA100LSTR | 8 Pin Surface Mount (1,000/Reel) |

## Pin Configuration

AC/DC Configuration


## Switching Characteristics of Normally Open (Form A) Devices



## Absolute Maximum Ratings @ $25^{\circ} \mathrm{C}$

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 350 | $\mathrm{~V}_{\mathrm{p}}$ |
| Reverse Input Voltage | 5 | V |
| Input Control Current <br> Peak (10ms) | 50 | mA |
|  | 1 | A |
| Input Power Dissipation ${ }^{1}$ | 150 | mW |
| Total Power Dissipation ${ }^{2}$ | 800 | mW |
| Isolation Voltage, Input to Output | 3750 | $\mathrm{~V}_{\text {rms }}$ |
| Operational Temperature | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

${ }^{1}$ Derate linearly $1.33 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$
$2^{2}$ Derate linearly $6.67 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at $+25^{\circ} \mathrm{C}$, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ $25^{\circ} \mathrm{C}$

| Parameter | Conditions | Symbol | Min | Tур | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Characteristics |  |  |  |  |  |  |
| Load Current |  |  |  |  |  |  |
| Continuous ${ }^{1}$ | - | $\mathrm{I}_{\mathrm{L}}$ | - | - | 120 | $m A_{\text {rms }} / m A_{\text {dc }}$ |
| Peak | 10 ms | $\mathrm{I}_{\text {LPK }}$ | - | - | $\pm 350$ | $m A_{P}$ |
| On-Resistance ${ }^{2}$ | $\mathrm{I}_{\mathrm{L}}=120 \mathrm{~mA}$ | $\mathrm{R}_{\mathrm{ON}}$ | - | - | 25 | $\Omega$ |
| Off-State Leakage Current | $\mathrm{V}_{\mathrm{L}}=350 \mathrm{~V}$ | $\mathrm{I}_{\text {LEAK }}$ | - | - | 1 | $\mu \mathrm{A}$ |
| Switching Speeds |  |  |  |  |  |  |
| Turn-On | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=10 \mathrm{~V}$ | $t_{\text {on }}$ | - | - | 5 | ms |
| Turn-Off |  | $\mathrm{t}_{\text {off }}$ | - | - | 5 | ms |
| Output Capacitance | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=50 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {OUT }}$ | - | 35 | - | pF |
| Input Characteristics |  |  |  |  |  |  |
| Input Control Current to Activate | $\mathrm{I}_{\mathrm{L}}=120 \mathrm{~mA}$ | $I_{\text {F }}$ | - | - | 5 | mA |
| Input Control Current to Deactivate | - | $\mathrm{I}_{\mathrm{F}}$ | 0.4 | 1.0 | - | mA |
| Input Voltage Drop | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $V_{F}$ | 0.9 | 1.2 | 1.5 | V |
| Reverse Input Current | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | $I_{R}$ | - | - | 10 | $\mu \mathrm{A}$ |
| Common Characteristics |  |  |  |  |  |  |
| Capacitance Input to Output | $\mathrm{V}_{10}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{10}$ | - | 3 | - | pF |

[^0]
## PERFORMANCE DATA*


*Unless otherwise noted, data presented in these graphs is typical of device operation at $25^{\circ} \mathrm{C}$
For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*


## Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard IPC/JEDEC J-STD-033.

| Device | Moisture Sensitivity Level (MSL) Classification |
| :---: | :---: |
| LAA100L/ LAA100LS / LAA100PL | MSL 1 |

## ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

## Soldering Profile

Provided in the table below is the Classification Temperature $\left(T_{C}\right)$ of this product and the maximum dwell time the body temperature of this device may be $\left(T_{C}-5\right)^{\circ} \mathrm{C}$ or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of J-STD-020 must be observed.

| Device | Classification Temperature $\left(\mathrm{T}_{\mathrm{c}}\right)$ | Dwell Time $\left(\mathrm{t}_{\mathrm{p}}\right)$ | Max Refilow Cycles |
| :---: | :---: | :---: | :---: |
| LAA100L | $250^{\circ} \mathrm{C}$ | 30 seconds | 1 |
| LAA100LS | $250^{\circ} \mathrm{C}$ | 30 seconds | 3 |
| LAA100PL | $260^{\circ} \mathrm{C}$ | 30 seconds | 3 |

## Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.


ROHS


MECHANICAL DIMENSIONS

## LAA100L


$\frac{\text { Dimensions }}{\mathrm{mm}}$
(inches)

## LAA100LS



PCB Land Pattern


## LAA100PL



PCB Land Pattern

$\frac{\text { Dimensions }}{\mathrm{mm}}$

## LAA100LSTR Tape \& Reel



## LAA100PLTR Tape \& Reel



## For additional information please visit our website at: www.ixysic.com

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[^1]6/22/2018


[^0]:    ${ }^{1}$ If both poles operate, the load current must be derated so that the package power dissipation value is not exceeded.
    ${ }^{2}$ Measurement taken within 1 second of on-time.

[^1]:    Specification: DS-LAA100L-R03
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