

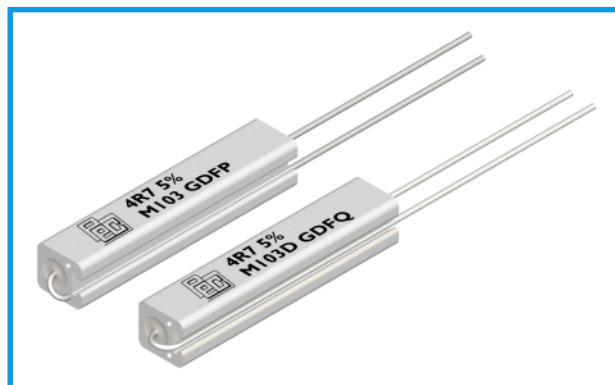


Slotted Ceramic Cased, Vertical Mounting / Dual Mounting

Series PGM/PGMD

Key Features

- 4W to 17W Power Rating, Ceramic Encased.
- PGM - Lead Through Hole for Vertical Mounting.
- PGMD-Long Lead for Horizontal & Vertical Mounting.
- High Insulation Resistance.
- High Surge Versions.
- Low Surface Temperature.
- Reference Standards.
 - IEC 115-1



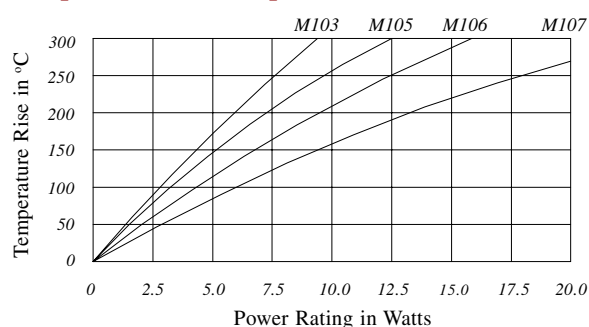
Electrical Specifications and Environmental Characteristics

Type	Power @70°C Watts	Ohmic Range Ohms		Additional Specifications	
		Min	Max		
M101/M101D	4	0R1	6K8	Applicable E-Series	Standard <10Ω ± 10%, >10Ω ± 5%, On Request ± 2%
M102/M102D	5	0R33	10K	Derating	From 70°C to 350°C
M103/M103D	7	0R47	22K	TCR -Low Values, IEC 115-1, 4.8.4.2, 2.2.20.2	450ppm/°C (Max)
M104/M104D	7	0R33	10K	TCR -Mid Values & High Values	Std.: <150ppm/°C ; On Request: 50ppm/°C, 20ppm/°C
M105/M105D	9	0R47	22K	Temperature Range	-55°C to 200°C
M106/M106D	11	0R82	22K	Climatic Category	55/200/56
M107/M107D	17	1R5	27K	Solvent Resistance	As per IEC 115.1- Clause 4.30(Test XA of IEC-68-2-45)

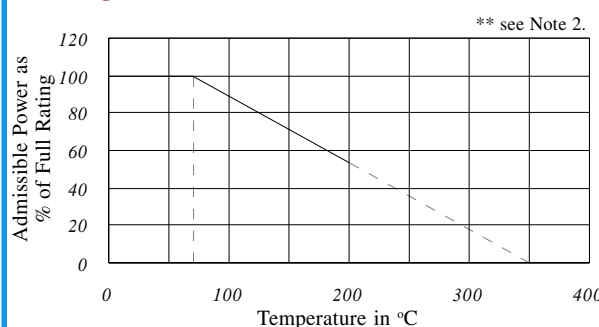
Performance Characteristics

Test Methods	Test Conditions	Test Limits
Insulation Resistance	At 500V DC, IEC115-1, Clauses-4.6, 2.2.19	10000 MΩ
Dielectric Strength	2000Vpeak for 1 min, IEC 115-1, Clause 2.2.17	No Break Down
Terminal Strength	Tensile Test , IEC115-1, Clause 4.16, Test Uai, IEC68-2-21	>50N
Solderability	As per MIL-STD 202F, Test 208, IEC 115, Clause 4.17.3	95% Coverage
Endurance at Rated Temperature	Rated Power @70°C(1.5hrs ON,0.5hrs OFF), IEC115-1, Clause 4.25	ΔR < 5% + 0R05
Damp Heat Steady State	90-95% RH @40°C Ambient Temperature for 56days, IEC115-1, Clause 4.24	ΔR < 5% + 0R05
Resistance to Soldering Heat	10 Seconds Dip in Solder Bath at 260°C, IEC115-1, Clause 4.18	ΔR < 1% + 0R05
Climatic Sequence	As per IEC 115.1, Clause 4.23	ΔR < 5% + 0R05

Temperature Rise Graphs

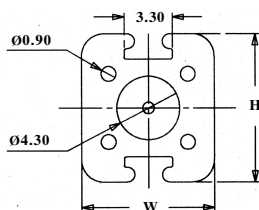


Derating Curve **



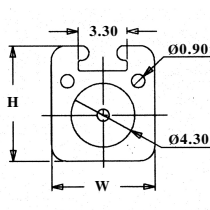
Dimensions

For M104, M105, M106, M107

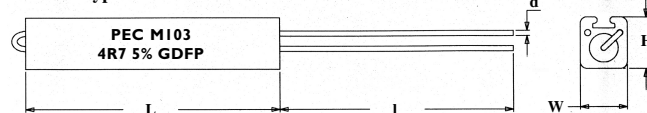


Do not Scale Drawings.
All dimensional tolerances in mm.

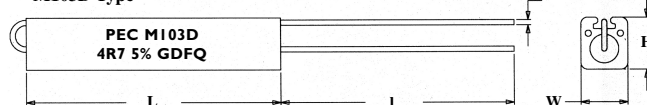
For M101, M102, M103



M103 Type



M103D Type



Dimensions (mm)

Type	L	W ± 0.5	H ± 0.5	I ± 3.0	d ± 0.05
M101/M101D	20.0 ± 1.0	7.0	8.0	32.0	0.80
M102/M102D	25.0 ± 1.0	7.0	8.0	32.0	0.80
M103/M103D	38.0 ± 1.0	7.0	8.0	32.0	0.80
M104/M104D	25.0 ± 1.0	9.0	10.0	32.0	0.80
M105/M105D	38.0 ± 1.0	9.0	10.0	32.0	0.80
M106/M106D	50.0 ± 1.5	9.0	10.0	32.0	0.80
M107/M107D	75.0 ± 2.0	9.0	10.0	32.0	0.80

Dimensions (Inches)

Type	L	W ± 0.02	H ± 0.02	I ± 0.118	d ± 0.002
M101/M101D	0.787 ± 0.04	0.276	0.315	1.260	0.0314
M102/M102D	0.984 ± 0.04	0.276	0.315	1.260	0.0314
M103/M103D	1.496 ± 0.04	0.276	0.315	1.260	0.0314
M104/M104D	0.984 ± 0.04	0.354	0.394	1.260	0.0314
M105/M105D	1.496 ± 0.04	0.354	0.394	1.260	0.0314
M106/M106D	1.960 ± 0.06	0.354	0.394	1.260	0.0314
M107/M107D	2.950 ± 0.08	0.354	0.394	1.260	0.0314

To Order - Please Specify

PEC Type.	Ohmic Value	Tolerance	Packing Style	Release Condition	Standard / Non-Std. Leads	TCR
M101/ M101D	0.1 Ohm » 0R1 / R10 1 Ohm » 1R0 1 KOhm » 1K0 10.7 KOhm » 10K7	2% » G 5% » J 10% » K	Bulk » B	Commercial » X	Standard » S 38mm / 1.5" » L Others » M	Standard » S Others » M Please Specify

A Sample Part No.: **M101 1K0 JBXSS / M101D 1K0 JBXSS**

Application Notes

- On request we undertake tests for Batch Acceptance to a specified Reference Standard.
- The Derating Curve specifies the maximum allowable Power at a particular ambient temperature while ensuring that the maximum surface temperature remains within the designed limit.
- When the Resistor is subjected to a Pulse Load, please ensure that the *average* Power dissipated remains below the rated Power specified.
- Resistor performance with Pulse Loads will have to be application tested. Please utilise our Pulse Application Questionnaire for selecting a suitable type or for requesting any design-in assistance from us.

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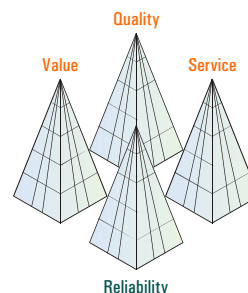
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